

BIAS VOLTAGE CIRCUIT WITH ULTRA LOW OUTPUT IMPEDANCE

ABSTRACT

A bias voltage circuit with ultra low output impedance. The circuit incorporates feedback to reduce the output impedance at both low and RF frequencies. The bias circuit outputs a bias signal for biasing an amplifier. The bias circuit includes an input stage that receives an input signal and produces the bias signal at an output terminal that is coupled to a gain stage. The bias circuit also includes a load coupled to the input stage at a first terminal, and a feedback circuit coupled between the first terminal and the gain stage.